#5/02

500.39590X00

18

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

MIYAKI et al.

Serial No.:

09/785,452

Filed:

February 20, 2001

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVI

AND METHOD OF MANUFACTURING THE SAME

Group:

2815

Examiner:

S. Clark

## RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents

April 18, 2002

Washington, D.C. 20231

Sir:

In response to the restriction requirement dated

March 27, 2002, applicants hereby elect the Group I, claims 1-10,

without traverse.

If the Examiner believes that there are any other points which may be clarified or otherwise disposed of, either by telephone discussion or by personal interview, the Examiner is invited to contact applicants' undersigned attorney at the number indicated below.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 500.39590X00).

Ву

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

GEM/kd

703/312-6600

Gregory E. Montone

Registration No. 28,141